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March 16, 1999

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Case No. 9683/22

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Takaaki Satoh et al.

Serial No: 09/125,958

Examiner:

Filed: August 26, 1998

Group Art Unit:

For: FRAME  
SYNCHRONIZATION  
CIRCUIT

### INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Applicants bring to the attention of the Examiner in charge of the above-captioned case the following information and references that may be relevant to the examination of the above-captioned patent application.

### U.S. PATENT

#### PATENT NO.

5,473,668

#### ISSUE DATE

Dec. 5, 1995

#### INVENTOR

Nakahara

FOREIGN PATENT DOCUMENTS

<u>PUBLICATION NO.</u>	<u>DATE</u>	<u>COUNTRY</u>
(1) JP, 7-298347, A	11/10/95	Japan
(2) JP, 6-164565, A	06/10/94	Japan
(3) JP, 7-87558, A	03/31/95	Japan
(4) JP, 7-284148, A	10/27/95	Japan

The above four prior art references are all in Japanese. Applicants are submitting a translation for each of prior art references (1) and (2). Prior art reference (3) is a Japanese counter part of above U.S. Patent No. 5,473,668. As to prior art reference (4), Applicants are submitting an English summary.

Respectfully submitted,

Dated: March 16, 1999

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